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Declaration under Rule 4.17:

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(54) Title: TAMPER-RESISTANT PACKAGING AND APPROACH USING MAGNETICALLY-SET DATA

(57) Abstract: A tamper-resistant packaging approach protects an integrated circuit elements (130-135) and then decrypted as a function of the state of a plurality of magnetically-responsive circuit elements (130-135) to take on a state that is used to encrypt the data. The state of these elements is again accordingly used to decrypt the data (e.g., as a key). When the magnetic particles realtered, for example, by removing a popular of the magnetically-responsive circuit elements is changed, thus remodering the state is again accordingly used to decrypt the data (e.g., as a key). When the magnetic particles are altered, for example, by removing a portion of the package, the state of one or more of the magnetically-responsive circuit elements is changed, thus rendering the state incapable of being used for decrypting the data.



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A. CLASSIFICATION OF SUBJECT MATTER - IPC 7 G06F1/00 -									
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B. FIELOS SEARCHED									
Minimum documentation searched (classification system followed by classification symbols) IPC 7 G06F G11C									
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